

Chanpyo Lee

prismarch1558@seoultech.ac.kr ▪ 232 Gongneung-ro, Nowon-gu, Seoul, South Korea, 01811

EDUCATION

2020 – Present Bachelor of Science in Electronic Engineering

Seoul National University of Science and Technology, Seoul, South Korea

RESEARCH EXPERIENCES

Research Assistant, Dept. of Electronic Engineering, Seoul National University of Science and Technology. Advisor: Jae-Won Nam (2025 ~ Present)

- Pipeline ADC measurement
- Dynamic-Latch based comparator Design
- Digital logic for DAC calibration
- Programming Language : Verilog HDL, Matlab, C
- EDA Tools : Cadence Virtuoso, Mentor Calibre, Xilinx Vivado, ModelSim

Tape-out

- 2025.09. TSMC 65nm LP RF, Cryogenic Qubit Controller ($2 \times 2.14 \text{ mm}^2$). (Digital block / Comparator)
- 2024.12. ETRI 500nm CMOS, 2nd order Sallen-Key LPF ($0.45 \times 0.45 \text{ mm}^2$). (Top integration / Amplifier)

HONORS / AWARDS

- N/A

LIST OF PUBLICATIONS

- N/A

TEACHING EXPERIENCE

- **Teaching Assistant**, Dept. of Electronic Engineering, Seoultech.
- Electronic Circuit(2), Fall 2025.

TRAINING ACTIVITIES

- 2025.02.10-02.14, [KIRD Education] Advanced semiconductor design (AVR Microcontroller & VCO).