

Inkwon Jang

Jangkwon34@seoultech.ac.kr ▪ 232 Gongneung-ro, Nowon-gu, Seoul, South Korea, 01811

EDUCATION

2020 – Present Bachelor of Science in Electronic Engineering

Seoul National University of Science and Technology, Seoul, South Korea

RESEARCH EXPERIENCES

Research Assistant, Dept. of Electronic Engineering, Seoul National University of Science and Technology. Advisor: Jae-Won Nam (2025 ~ Present)

- Current steering DAC
- Direct digital frequency synthesizer & pulse shaping module for qubit controller
- Programming Language : Verilog HDL, Matlab, Python
- EDA Tools : Cadence Virtuoso, Mentor Calibre, Altium Designer

Tape-out

- 2025.09. TSMC 65nm LP RF, Cryogenic Qubit Controller ($2 \times 2.14 \text{ mm}^2$). (Top integration / Digital block / Clock buffer)
- 2025.06. ETRI 500nm CMOS, Binary Instruction SRAM ($1.78 \times 1.78 \text{ mm}^2$). (Top integration / Decoder)

HONORS / AWARDS

- ETRI President's Award, The 1st Mychip Contest Autumn Annual Conference of IEIE, 2025, Nov. 2025.
- Chair's Award (2025 Capstone Design), Seoul National University of Science and Technology, College of Information and Communication, Dec. 2025.

LIST OF PUBLICATIONS

- S. -H. Kim, **I. -K. Jang**, Y. Kim, J.-Y. Park, J.-W. Nam, "Implementation of Second-Order Sigma-Delta ADC and Digital Filter using Switched Capacitor Circuit," Summer Annual Conference of IEIE, Jeju, Jun. 2025.

TEACHING EXPERIENCE

- **Teaching Assistant**, Dept. of Electronic Engineering, Seoultech.
- Electronic Circuit (1), Spring 2025.
- Electronic Circuit (2), Fall 2025.

TRAINING ACTIVITIES

- 2024.08.08-08.09, [IDEC Education] CMOS Digital Integrated Circuit Design.
- 2025.02.10-02.14, [KIRD Education] Advanced semiconductor design (AVR Microcontroller & VCO).