

Jeong-Taek Kim

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EDUCATION

2021 – Present Bachelor of Science in Electronic Engineering
Seoul National University of Science and Technology, Seoul, South Korea

RESEARCH EXPERIENCES

Research Assistant, Dept. of Electronic Engineering, Seoul National University of Science and Technology. Advisor: Kyung-Sik Choi (2024.06 ~ 2025.04)

- Radio Frequency (RF) Bidirectional Distributed Amplifier.

Research Assistant, Dept. of Electronic Engineering, Seoul National University of Science and Technology. Advisor: Jae-Won Nam (2025.04 ~ Present)

- Current steering Digital-to-Analog Converter (DAC) for qubit controller.
- Programming Language : Verilog HDL, Matlab
- EDA Tools : Cadence Virtuoso

Tape-out

- 2025.09. Samsung 28nm Bulk CMOS, DAC for qubit controller ($1.5 \times 4.0 \text{ mm}^2$). (DAC, Top integration)
- 2025.05. Samsung 28nm Bulk CMOS, Ultra-Wideband Bidirectional Distributed Amplifier ($2.4 \times 1.0 \text{ mm}^2$). (Revision)
- 2024.11. Samsung 28nm Bulk CMOS, Ultra-Wideband Bidirectional Distributed Amplifier ($2.4 \times 1.0 \text{ mm}^2$). (Top cell custom)

LIST OF PUBLICATIONS

Journal

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Conference

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TRAINING ACTIVITIES

- 2024.08.20-08.23, [KIRD Education] Advanced semiconductor design (VCO).

- 2024.08.27-08.30, [KIRD Education] Advanced semiconductor design (RF PA).