

Minseo Yu

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EDUCATION

- 2026 – Present Master of Science Candidate in Electronic Engineering
 Seoul National University of Science and Technology, Seoul, South Korea
- 2021 – 2025 Bachelor of Science in Intelligent Electronic Engineering
 Kookmin University, Seoul, South Korea

RESEARCH EXPERIENCES

Research Assistant, Dept. of Electronic Engineering, Seoul National University of Science and Technology. Advisor: Jae-Won Nam (2025.10 ~ Present)

- Time-interleaved Pipeline ADC.
- Pipeline ADC with Digital background calibration.
- Programming Language : Verilog HDL, Matlab
- EDA Tools : Cadence Virtuoso, Xilinx Vivado

TRAINING ACTIVITIES

- 2024.01.06-01.19, [The Korean Institute of Electrical and Electronics Engineers] Semiconductor School II
- 2025.04 .01-07.10, [Sungkyunkwan University] Advanced System Semiconductor Design Academy